

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	4470	cvs and (electric\$5 adj3 (data or layout or circuit or schematic))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:01
S2	13	S1 and (master adj3 file)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 08:35
S3	1412	S1 and (computer ad3 network)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 08:35
S4	784	S3 and modif\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 08:36
S5	152	S4 and manag\$	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 08:36
S6	14	(concurrent adj3 version adj3 system) and (electric\$5 adj3 (data or layout or circuit or schematic))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 14:06
S7	10	((("5559997") or ("5592392") or ("5475804") or ("4965741") or ("5418728") or ("6177942") or ("6721614") or ("5519630") or ("6851094") or ("4862376"))).PN.	USPAT; USOCR	OR	OFF	2005/09/13 14:07
S8	51	orCad	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:07

S9	731	circuit and (team adj3 member) and (modif\$6 or change or (check adj3 out))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:17
S10	267	S9 and schematic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:09
S11	606	circuit and ((plurality or multiple) adj3 users) and (modif\$6 or change or alter\$5 or (check adj3 out)) and (file adj7 shar\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:19
S12	491	circuit and ((plurality or multiple) adj3 users) and (modif\$6 or change or alter\$5 or (check adj3 out)) and (file adj7 shar\$4) and manag\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:20
S13	50	S12 and (tracking adj3 changes)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:20
S14	593	circuit and ((plurality or multiple) adj3 users) and (modif\$6 or change or alter\$4 or (check adj3 out)) and (file adj7 shar\$4) and network	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:54
S15	1	("6094654").PN.	USPAT; USOCR	OR	OFF	2005/09/15 10:19
S16	76	(circuit adj3 design) and ((plurality or multiple) adj3 users) and (modif\$6 or change or alter\$4 or (check adj3 out)) and (file adj7 shar\$4) and network	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 15:55

S17	53	("4141078" "5201047" "5280614" "5287459" "5297279" "5321605" "5333312" "5333315" "5333316" "5396616" "5418949" "5504885" "5515488" "5548749" "5560005" "5684987" "5684989" "5689697" "5724581" "5737737" "5745747" "5778365" "5832484" "5845292" "5864875" "5873085").PN. OR ("6094654"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/15 09:44
S18	1	("5333312").PN.	USPAT; USOCR	OR	OFF	2005/09/15 09:45
S19	1	("5333316").PN.	USPAT; USOCR	OR	OFF	2005/09/15 09:45
S20	1	("6094654").PN.	USPAT; USOCR	OR	OFF	2005/09/15 10:19
S21	1	S20 and pointer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/15 10:19
S22	46	("5333316").URPN.	USPAT	OR	OFF	2005/09/15 10:31
S23	64	("3617714" "4827427" "4882690" "5051938" "5111413" "5220657" "5301318" "5333316" "5335320" "5341308" "5367468" "5436849" "5442790" "5473547" "5499192" "5504885" "5513124" "5519866" "5526517" "5537295" "5541849" "5544067" "5550782" "5583749" "5603043" "5623418" "5636133" "5661660" "5670895" "5691912" "5696454" "5712794" "5721912" "5724251" "5754441" "5761079" "5784636" "5812847" "5825661" "5826265" "5831863" "5856926" "5859776" "5867396" "5867399" "5870308" "5875112" "5896521" "5903475" "5983277" "6026226" "6102964" "6120550" "6134705" "6161211" "6182247").PN. OR ("6298319"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/15 15:25
S24	0	"603395".an.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/15 15:25

S25	5	"603395".ap.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/15 15:26
S26	5	"333965".ap.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/15 15:26
S27	0	"60333965".ap.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/15 15:26
S28	0	"60/333965".ap.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/15 15:26
S29	55	schematic adj3 image adj3 file	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 11:46
S30	19	circuit and schematic adj3 image adj3 file	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 12:53
S31	0	autored and schematic adj3 image adj3 file	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 12:53
S32	0	autored	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 12:53
S33	0	autored	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/16 12:53
S34	0	AUTORED	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/16 12:53



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1 [Knowledge based approach for the verification of CAD database generated by an automated schematic capture system](#)

J. Y. Tou, W. H. Ki, K. C. Fan, C. L. Huang

October 1987 **Proceedings of the 24th ACM/IEEE conference on Design automation**Full text available: pdf(765.41 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

CAD database generated by an automatic schematic capture system needs to be verified before it can be used in design automation. This verification is best performed by a knowledge-based expert system. Presented in this paper is the design of a knowledge-based system for the verification of CAD database generated by AUTORED. Database-driven, pattern-directed inference technique is employed to identify and correct erroneous data records due to misrecognition. This knowledge-based verification ...

2 [Automatic input and interactive editing systems of logic circuit diagrams](#)

Mitsuo Ishii, Yoshikazu Ito, Michiko Iwasaki, Masanari Yamamoto, Sadao Kodama

June 1981 **Proceedings of the 18th conference on Design automation**Full text available: pdf(583.60 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper discusses automatic input and interactive editing systems of logic circuit diagrams. Automatic input is based on pattern recognition, and interactive editing is executed through a graphic display. The system is implemented on a FACOM M-180 II and a PANAFACOM U-400. System overview, hardware configuration, pattern recognition algorithms, editing system, data-base, as well as current results are described.

3 [Coupling graphics and circuit analysis techniques](#)

J. L. Malakoff

January 1967 **Proceedings of the 4th conference on Design automation**Full text available: pdf(931.56 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The purpose of this paper is to describe an application program written by the International Business Machines Corporation to couple the existing Electronic Circuit Analysis Program(1) with the IBM 2250 cathode ray tube display scope. The program which, in this paper, will be referred to as 2250 ECAP is a Type III program available from IBM(2). 2250 ECAP has been in use at Beckman Instruments, Inc., Fullerton, California, for approximately four months. This paper will d ...